

## PATENT ABSTRACTS OF JAPAN

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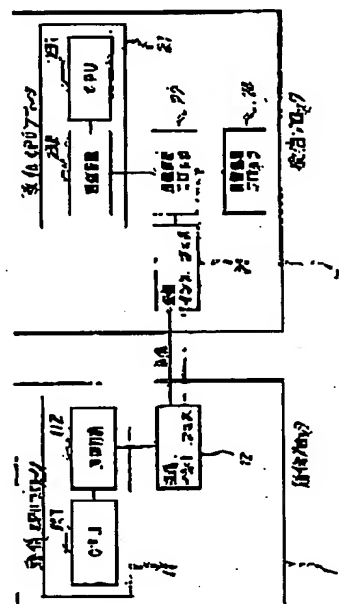
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## (54) SERIES DATA COMMUNICATION EQUIPMENT

## (57)Abstract:

**PURPOSE:** To lower the probability of an erroneous communication by completing a data transfer in CPUs or between each peripheral circuit thereof within a prescribed time.

**CONSTITUTION:** On a transmitting side, a transmitting interface 12 for transmitting N times transmitting data of the same frame as series data is provided, and on a receiving side, a receiving interface 21 for receiving the transmitted data, a receiving and analyzing block for analyzing normal received data from the received data of received N frames, and an abnormality processing block for executing an abnormality processing, when the normal received data cannot be analyzed are provided.



## LEGAL STATUS

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